

## IMAGE MAGNIFYING CIRCUIT

## ABSTRACT

An image magnifying circuit ~~comprises~~ is made up of a frame memory 12 for storing input image data, a coefficient memory 18 in which ~~in advance~~ filter coefficients corresponding to a plurality of magnification ratios are ~~store~~ stored, a non-linear magnification controller 20 for outputting an enable signal for reading the corresponding image data from the frame memory 12 and a coefficient selecting address AD3 for reading the corresponding filter coefficient from the coefficient memory 18, on the basis of ~~an~~ a area width  $w$  predetermined to divide a display screen into  $n$  parts and ~~the a predetermined magnification~~ a predetermined magnification ~~predetermined~~ for the  $n$  ~~areas~~ parts, and a filter 14 for filtering the image data from the frame memory 12 on the basis of the filter coefficients from the coefficient memory 18 to output the image data on the image which ~~is~~ is non-linearly magnified in a horizontal direction. These filter coefficients correspond to the magnification ratio which are set for the  $n$  ~~areas~~ parts of the display screen.